A NEW FFT ALGORITHM AND ITS IMPLEMENTATION ON THE DSP96002

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ABSTRACT

This work presents a new algorithm for computing the discrete Fourier transform (DFT). This fast Fourier transform (FFT) algorithm is obtained through decomposition of the Fourier matrix representing the DFT operator into a product of sparse matrices not all square matrices. The algorithm is based on additive properties of the input and output indexing sets of the Fourier transformation. Mathematical formulations of the algorithm are presented using tensor product algebra. Properties of this algebra are used to assist in the adaptation of the algorithm to the DSP96002 microprocessor architecture. This results in efficient implementations which take into account the inherent software and hardware features of this microprocessor.

1. Introduction

Computing the DFT y of an n-point complex sequence x is equivalent to the computation of the matrix product

$$y = F_n \cdot x \tag{1}$$

where F_n is the Fourier matrix of order n defined by

$$F_n = [w_n^{k\ell}]_{\substack{0 \le k \le n \ 0 \le \ell \le n}}^{0 \le k \le n}, \quad w_n = e^{-2\pi j/n}, \quad j = \sqrt{-1}$$
 (2)

Straightforward computation (direct method) of Eq. (1) requires n^2 multiplications and n(n-1) additions. When n is a composite number of the form $n=r^2\cdot m$, r and m any positive integers greater than one, the Fourier matrix F_n may be factored into a product of sparse matrices, not all square, which reduces the number of arithmetic computations (additions and multiplications) during a DFT calculation. We set out to describe this factorization technique by using properties of tensor product algebra. We first present some definitions. Then, we proceed to describe the algorithm for the specific case of $n=2^2\cdot m$. This is done for the purpose of clarity; but, then the algorithm is generalized.

Define the tensor product $A \otimes B$ of the matrices A and B as $A \otimes B = [a_{(i,j)} \cdot B]$; thus,

$$A \otimes B = \begin{bmatrix} a_{(0,0)}B & \dots & a_{(0,s-1)}B \\ a_{(1,0)}B & \dots & a_{(1,s-1)}B \\ \vdots & \ddots & \vdots \\ a_{(r-1,0)}B & \dots & a_{(r-1,s-1)}B \end{bmatrix}$$
(3)

where $a_{(i,j)}$ is the entry in the *i*-th row and *j*-th column of the matrix

$$A = [a_{(i,j)}]_{\substack{0 \le i < r \\ 0 \le i \le s}} \tag{4}$$

$$A \otimes B = C = [a_{(p,q)}] \otimes [b_{(r,\bullet)}] = [c_{(t,u)}]$$
 (5)

where

$$c_{(t,u)} = c_{(p,r;q,s)} = a_{(p,q)} \cdot b_{(r,s)}$$
 (6)

The double indexation of the entries of the ${\cal C}$ matrix is enacted lexicographically.

Define the $m \times 1$ vector U_m as

$$U_{m} = \begin{bmatrix} u_{0} \\ u_{1} \\ u_{2} \\ \vdots \\ u_{m-1} \end{bmatrix}, \quad u_{0} = u_{1} = u_{2} = \ldots = u_{m-1} = 1 \quad (7)$$

If A_n is a square matrix of order n, then

$$U_m^T \otimes A_n = \left[\underbrace{A_n \quad A_n \quad A_n \quad \dots \quad A_n}_{m} \right] \tag{8}$$

If A_n is symmetric, then

$$(U_m^T \otimes A_n)^T = U_m \otimes A_n^T = U_m \otimes A_n = \begin{bmatrix} A_n \\ A_n \\ A_n \\ \vdots \\ A_n \end{bmatrix}$$
(9)

Define I_n as the identity matrix of order n. Thus, we have

$$(I_{\bullet} \otimes F_{r}) = diag[F_{r} F_{r} \dots F_{r}]$$
 (10)

A stride by s permutation matrix $P_{n,s}$ of order $n = r \cdot s$ is defined by

$$P_{n,s} \cdot d = [d_0, d_s, d_{2s}, \dots, d_{1s}, d_{s+1}, \dots, d_{(r-1)s+(s-1)}]^T$$
(11)

$$d = [d_0, d_1, d_2, \dots, d_{s-1}, d_s, d_{s+1}, \dots, d_{(r-1)s+(s-1)}]^T$$
 (12)

When n=2s, the permutation matrix $P_{n,s}$ is called the perfect-shuffle matrix. If s=2, $P_{n,2}$ is called the even-odd permutation matrix.

Using the matrix equality

$$P_{n,s}^{T} = P_{n,s}^{-1} = P_{n,r}, \quad n = r \cdot s,$$
 (13)

the following result is established:

$$(P_{n,s} \cdot d)^T = d^T \cdot P_{n,s}^{-1} = d^T \cdot P_{n,r} = [d_0, d_s, \dots, d_{n-1}]$$
 (14)

When the Fourier matrix F_n , of order $n = r \cdot s$, is multiplied on the right by the permutation matrix $P_{n,r}$, the product is defined as the matrix $C_{n,s}$.

$$C_{n,s} = F_n P_{n,r} = F_n P_{n,s}^{-1} \tag{15}$$

The diagonal matrix $D_{n,r}$, of order r is defined by

$$D_{n,r} = \text{diag}\left[1, w_{n,r}w_{n}^{2}, w_{n}^{3}, \dots, w_{n}^{r-1}\right], \qquad w_{n} = e^{-2\pi j/n}$$
(16)

The twiddle factor (phase factor) matrix $T_{n,s(r)}$, of order n, is defined as the direct sum of s diagonal matrices $D_{n,n/s}$ of order $r = \frac{n}{2}$:

$$T_{n,s(r)} = \sum_{j=0}^{s-1} \oplus D_{n,n/s}^{j} \tag{17}$$

If n = r.s, then

$$T_{n,s(n)} = T_{n,s} = \sum_{j=0}^{s-1} \oplus D_{n,n/s}^{j} = \sum_{j=0}^{s-1} \oplus D_{n,s}^{j}$$
 (18)

2. Description of the $n = 2^2 \cdot m$ Case

Observation: If n = 4m, then

$$F_n = (U_{2m}^T \otimes F_2 \otimes I_{2m})(T_{mn,2m(n)})(I_{2m} \otimes U_m \otimes F_2)P_{n,2m}$$
(19)

Proof:

Direct matrix multiplication shows that the matrix $C_{n,2m}$ can be factorized into a product of an $n \times mn$ matrix G times an $mn \times n$ matrix H:

$$C_{n,2m} = G_{n \times mn} \cdot H_{mn \times n} \tag{20}$$

Here, we set the matrix H to be

$$H_{mn\times n} = (I_{2m} \otimes U_m \otimes F_2) \tag{21}$$

and

$$G_{n \times mn} = \begin{bmatrix} I_{2m} & D_{n,2m} & \dots & D_{n,2m}^{2m-1} \\ I_{2m} & w^{2m} D_{n,2m} & \dots & w^{2m} D_{n,2m}^{2m-1} \end{bmatrix}$$
(22)

We then proceed to extract a special twiddle factor $T_{mn,2m(n)}$ from $G_{n\times mn}$:

Rewriting the above matrices in tensor products form, we obtain:

$$G_{n\times mn} = (U_m^T \otimes F_2 \otimes I_{2m})(T_{mn,2m(n)}) \tag{24}$$

Since,

$$F_n = C_{n,2m}P_{n,2m} = G_{n\times mn}H_{mn\times n}P_{n,2m}$$
, (25)

we get the desired result

$$F_{n} = (U_{m}^{T} \otimes F_{2} \otimes I_{2m})(T_{mn,2m(n)})(I_{2m} \otimes U_{m} \otimes F_{2})P_{n,2m}$$
(26)

3. Variants of the Algorithm

Variants of the $2^2 \cdot m$ algorithm may be formulated by using some properties of tensor products algebra. For instance, we can expressed Eq.(19) in a form which clearly identifies the number additions required for performing the overall computation. We present a couple of examples. The first example is a decimation in time (DIT) tensor product formulation. The second example is a decimation in frequency (DIF) formulation. We proceed to describe the basic operations performed on the matrix $H_{mn \times n}$ and the matrix product $G_{n \times mn} T_{mn, 2m(n)}^{-1}$ to arrive at the desired result.

$$H_{mn\times n} = (I_{2m} \otimes U_m \otimes F_2)$$

$$= I_{2m} \otimes (U_m \cdot U_1 \otimes I_2 \cdot F_2)$$

$$= I_{2m} \otimes (U_m \otimes I_2) \cdot (U_1 \otimes F_2)$$

$$= I_{2m} \cdot I_{2m} \otimes (U_m \otimes I_2) \cdot F_2$$

$$= (I_{2m} \otimes U_m \otimes I_2)(I_{2m} \otimes F_2)$$
(27)

$$G_{n \times mn} T_{mn,2m(n)}^{-1} = (U_m^T \otimes F_2 \otimes I_{2m})$$

$$= (U_1^T \cdot U_m^T \otimes F_2 \cdot I_2) \otimes I_{2m}$$

$$= (U_1^T \otimes F_2)(U_m^T \otimes I_2) \otimes I_{2m} \qquad (28)$$

$$= F_2 \cdot (U_m^T \otimes I_2) \otimes I_{2m} \cdot F_{2m}$$

$$= (F_2 \otimes I_{2m})(U_m^T \otimes I_2 \otimes I_{2m})$$

Thus,

$$F_n = F_{4m} = (F_2 \otimes I_{2m})((U_m^T \otimes I_2) \otimes I_{2m}).$$

$$T_{mn,2m(n)}(I_{2m} \otimes (U_m \otimes I_2)(I_{2m} \otimes F_2)P_{4m,2m}$$
 (29)

This expression of the F_{4m} matrix identifies the number of required additions to be $2(2 \cdot 2m) = 8m$. From the twiddle factor matrix we can also identify the number of required multiplications to be 4m(m-1).

By taking the matrix transpose of F_{4m} , we obtain decimation in frequency algorithms: From Eq.(19),

$$F_{4m} = P_{4m,2}(I_{2m} \otimes U_m^T \otimes F_2) T_{mn,2m(n)}(U_m \otimes F_2 \otimes I_{2m})$$
 (30)

From (30).

$$F_{4m} = P_{4m,2}(I_{2m} \otimes F_2)(I_{2m} \otimes (U_m^T \otimes I_2)) \cdot T_{mn,2m(n)}((U_m \otimes I_2) \otimes I_{2m})(F_2 \otimes I_{2m})$$
(31)

4. General r2 · m Algorithm

Here we present a generalization of the $2^2 \cdot m$ algorithm for the case when $n=r^2 \cdot m$ and r is any integer. This will result in a two-factor formulation $(n=r \cdot s)$ of the algorithm. We can use this formulation in a recursive manner to obtain a general formulation of the algorithm for the case when n is a composite of the form $n=a_{k-1}^{2^{k-1}} \cdot a_{k-2}^{2^{k-2}} \dots a_2^{2^k} \cdot a_1^2 \cdot a_0$. For the two-factor formulation we use a "divide and conquer" approach where the input and output indexing sets are represented as two dimensional arrays. We use this indexation scheme in Eq.(1) and properties of tensor product algebra to obtain the desired representation.

We start by considering the length of the input sequence x to be a composite integer of the form $n=r \cdot s$. Using the identities $\ell=\ell_1+s\ell_0$ and $k=k_1+sk_0$, the index product $k\ell$ given in Eq.(1) is expanded into

$$k\ell = (k_1 + sk_0)(\ell_1 + s\ell_0) = k_1\ell_1 + sk_1\ell_0 + sk_0\ell_1 + s^2k_0\ell_0$$
 (32)

where $0 \le k_1, \ell_1 < s$ and $0 \le k_0, \ell_0 < r$. Substituting this product expansion into Eq.(1) results in the following expression

$$y(k_1, k_0) = \sum_{\ell_1=0}^{s-1} \sum_{\ell_0=0}^{r-1} w_n^{(k_1\ell_1 + sk_1\ell_0 + sk_0\ell_1 + s^2k_0\ell_0)} x(\ell_1, \ell_0),$$
(33)

which we can rewrite as

$$y(k_1, k_0) = \sum_{\ell_1=0}^{s-1} w_r^{k_0 \ell_1} \left\{ w_{rs}^{k_1 \ell_1} \left[\sum_{\ell_2=0}^{r-1} w_r^{k_1 \ell_0} x_1(\ell_0, \ell_1) \right] \right\}$$
(34)

Throughout, we think of the sequences x and y as onedimensional vectors with a two-dimensional indexing scheme. This allows us to relate the sequences $x_1(\ell_1, \ell_0)$ and $x(\ell_0, \ell_1)$ through the identity

$$P_{n,s}x(\ell_1,\ell_0) = x_1(\ell_0,\ell_1), \quad 0 \le \ell_0 < r, \ 0 \le \ell_1 < s \quad (35)$$

We let $s = r \cdot m$. After some manipulation, we obtain the following expression for the inner bracket of Eq.(34)

$$y_1(k_1, \ell_1) = (U_m \otimes F_r)x_1(\ell_0, \ell_1) = (U_m \otimes I_r)F_rx_1(\ell_0, \ell_1)$$

$$(36)$$

$$y_1(k_1, \ell_1) = (I_{\bullet} \otimes U_m \otimes I_r)(I_{\bullet} \otimes F_r)x_1(\ell_0, \ell_1)$$

$$(37)$$

The twiddle or phase-factor $w_n^{k_1\ell_1}$ can be expressed in matrix form in the following manner

$$T_{\mathfrak{s}^2,\mathfrak{s}(n)} = \sum_{i=0}^{\mathfrak{s}-1} \oplus \left[D_{n,m}^r \otimes D_{n,r} \right]^j \tag{38}$$

Thus, we can write

$$y_2(k_1, \ell_1) = T_{s^2, s(n)} y_1(k_1, \ell_1) \tag{39}$$

Finally, the expression

$$y(k_1, k_0) = \sum_{\ell_1=0}^{s-1} w_r^{k_0 \ell_1} y_2(k_1, \ell_1)$$
 (40)

is formulated in tensor product notation in the following way

$$y(k_1, k_0) = (F_r \otimes I_s)((U_m^T \otimes I_r) \otimes I_s)y_2(k_1, \ell_1)$$
 (41)

Combining, we obtain the general expression of the $r^2 \cdot m$ algorithm

$$y(k_1, k_0) = (F_r \otimes I_{\bullet})((U_m^T \otimes I_r) \otimes I_{\bullet}) \cdot$$

$$T_{\bullet^2, \bullet(n)}((I_{\bullet} \otimes U_m) \otimes I_r)(I_{\bullet} \otimes F_r) P_{n, \bullet} x(k_1, k_0)$$
(42)

5. DSP Microprocessor Implementation

Analysis of the implementation process guided us to devote a close attention to the manner in which twiddle or phase-factor operations are performed. For the special case of the $2^2 \cdot m$ algorithm, the major bulk of the multiplication operations reside in the computation of the twiddle factors. This can be evidenced in Eq.(19) and Eq.(29) above. For this reason special macros are developed for the generation of lookup tables. Properties of tensor or Kronecker product are used to go from Eq.(19) to Eq.(29). The latter equation reveals the hidden butterfly operations. These operations are performed using butterfly kernels built using the FADDSUB instruction as the basic building block.

We looked very closely at implementation procedures for the expressions $((U_m^T \otimes I_2) \otimes I_{2m})$ and $(I_{2m} \otimes (U_m \otimes I_2))$. They, in essence, control the data flow of the algorithm. The stride permutation matrix $P_{4m,2m}$ is simply the perfect-shuffle permutation. This operation was implemented using special properties of the Address Generation Unit (AGU).

We then proceeded to identify efficient implementation procedures for expressions of the form $(I_s \otimes F_r)$ and $(F_s \otimes I_r)$ which we call Fourier factors. These factors play a very important role during the implementation of linear, shift-invariant, finite impulse response (LSI-FIR) filters as can be seen in the following mathematical description of these filters using tensor product algebra.

Suppose that the sequence x is the input to the LSI-FIR system T_h . The output sequence y is given by

$$y = T_h(x) = h \bigcirc_n x \tag{43}$$

where \bigcirc_n denotes cyclic convolution.

Applying (DFT) operator (represented by the Fourier matrix F_n) to the above expression results in

$$F_n(y) = F_n(T_h(x)) = F_n(h) \odot F_n(x) \qquad (44)$$

This is the same as

$$(F_n \circ T_h)(x) = (F_n(h) \odot F_n)(x) \tag{45}$$

here, \circ denotes composition of operators, and \odot represents a point-wise multiplication of two sequences. Since the choice of x was arbitrary, we obtain the following important result

$$F_n \odot T_h = F_n(h) \odot F_n \tag{46}$$

Emphasizing the diagonalization of T_h by the action of the DFT operator gives us the following expression

$$F_n T_h F_n^{-1} = F_n(h) \odot I_n \tag{47}$$

The expression $F_n(h) \odot I_n$ is denoted by $D_{F(h)} = D_{\widehat{h}}$, where a matrix representation of $D_{\widehat{h}}$ is given by

$$D_{\widehat{h}} = \begin{bmatrix} \widehat{h}_0 & & & & \\ & \widehat{h}_1 & & & \\ & & \widehat{h}_2 & & \\ & & & \ddots & \\ & & & & \widehat{h}_{n-1} \end{bmatrix}, \widehat{h}_j = (F_n(h))(j) \quad (48)$$

Here, $j \in \mathbb{Z}/n = \{0, 1, 2, \dots, n-1\}$ Thus, we can write

$$T_h = F_n^{-1} D_{\widehat{L}} F_n \tag{49}$$

This last expression serves as the basis for the formulations of LSI-FIR systems using FFT algorithms. This is accomplished by computing, both, the discrete Fourier transform and its inverse using fast algorithms. For example Suppose that h is the impulse response of the LSI-FIR system T_h . If n is a composite of the form $n=r\cdot s$, $(s=r\cdot m)$ we can factor the matrix $H=F_n^{-1}D_{\widehat{h}}F_n$ representing the system T_h in a form which uses the $r^2\cdot m$ decimation in frequency (DIF) algorithm for the computation of the Fourier transform; and the $r^2\cdot m$ decimation in time (DIT) algorithm for the computation of its inverse. This will allow as to precompute $\widetilde{D}_{\widehat{h}} \equiv P_{n,s}D_{\widehat{h}}P_{n,s}^{-1}$

We can partition the matrix H so that we obtain a block circulant matrix with circulant blocks. This partition is always circulant. The block size can be chosen to be either r or s. Thus writing a a partition of H into submatrices H_j , $0 \le j < r$ of block size s results in the following

$$H = \sum_{j \in \mathbb{Z}/r} S_r^j \otimes H_j \tag{51}$$

The submatrices, as we have stated above, are circulant; hence, they may represent lower order LSI-FIR systems.

Diagonalizing the shift operator S_r and the matrices H_j simultaneously produces the following known result

$$H = \sum_{j \in \mathbb{Z}/\tau} [F_{\tau}^{-1} D_{\widehat{S_{\tau}} \widehat{\delta}} F_{\tau}] \otimes [F_{\bullet}^{-1} D_{\widehat{h}}(j) F_{\bullet}] \qquad (52)$$

where $D_{\widehat{S_r}\delta}$ represents the diagonalized shift operator S_r , δ is the unit sample sequence of length r, and $D_{\widehat{h}}(j)$ is a diagonal matrix of order s corresponding to the diagonalization of the circulants H_j . Using properties of tensor product algebra, we obtain

$$H = \sum_{j \in \mathbb{Z}/r} (F_r^{-1} \otimes F_e^{-1}) [D_{\widehat{S_r \delta}} \otimes D_{\widehat{h}}(j)] (F_r \otimes F_e)$$
 (53)

Also

$$H = (F_r \otimes F_{\bullet})^{-1} \left[\sum_{j \in Z/r} (D_{\widehat{S_{\bullet} \delta}} \otimes D_{\widehat{h}}(j)) \right] (F_r \otimes F_{\bullet})$$
 (54)

Using, again, tensor product properties, we write

$$H = \mathcal{B}_{[r,s]}^{-1} \Big[\sum (D_{\widehat{S_r \delta}} \otimes D_{\widehat{h}}(j) \Big] \mathcal{B}_{[r,s]}$$
 (55)

where

$$\mathcal{B}_{[r,s]} \equiv (F_r \otimes I_s)(I_r \otimes F_s) = (I_r \otimes F_s)(F_r \otimes I_s)$$
 (56)

6. Conclusion

An algorithm has been presented for the computation of the discrete Fourier transform. Tensor product algebra was used as a tool to aid in the implementation of this algorithm on the DSP96002 microprocessor. We used properties of this algebra to match this algorithm to the architecture of this microprocessor, exploiting its inherent software and hardware features. There exists a clear demarcation between the various processing stages of this algorithm, i.e., pre-addition, data routing, multiplication, etc. This feature makes it suitable for expressing the algorithm in signal flow graph (SFG) form. This, in turn, helps in identifying feasible implementations for real time processing.

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