

Sensores de Posición

INEL 5205 - Instrumentación

- Aplicaciones:
 - data de posición para servo-mecanismos, robots de ensamblaje
 - *machine tools*
 - plumilla de una impresora, etc.
- Puede requerir alta precisión
- Métodos:
 - Potenciometros industriales
 - *Linear-variable diff. transformers (LVDT)*
 - codificadores (*encoders*) ópticos

Single-Turn
Wirewound
Potentiometers

PD200 Series



Special features

- very small dimensions
- 200×10^3 movements
- excellent linearity $\pm 0.4\%$
- very robust
- highest protection class

Industrial Pots

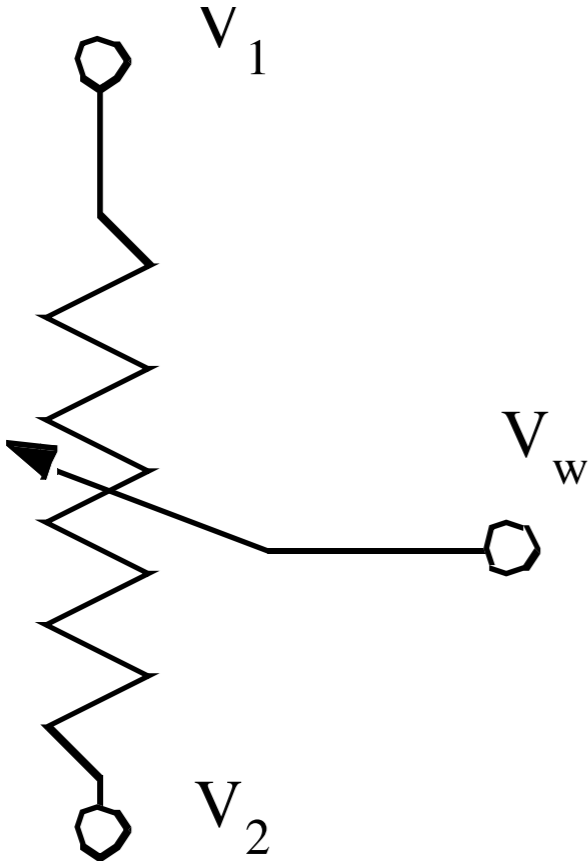
Electrical angle: 318°

Repeatability: 0.11%

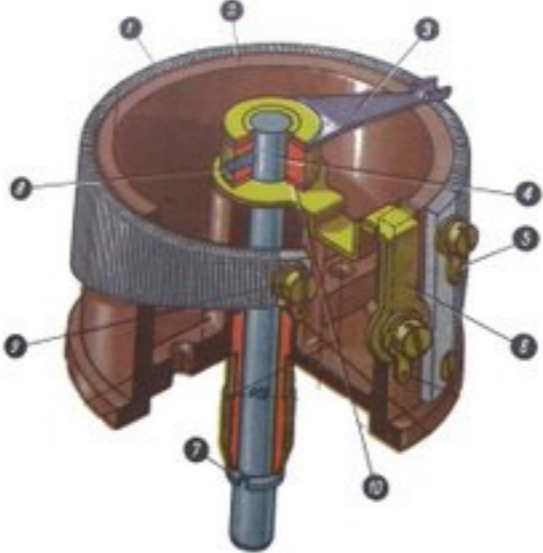
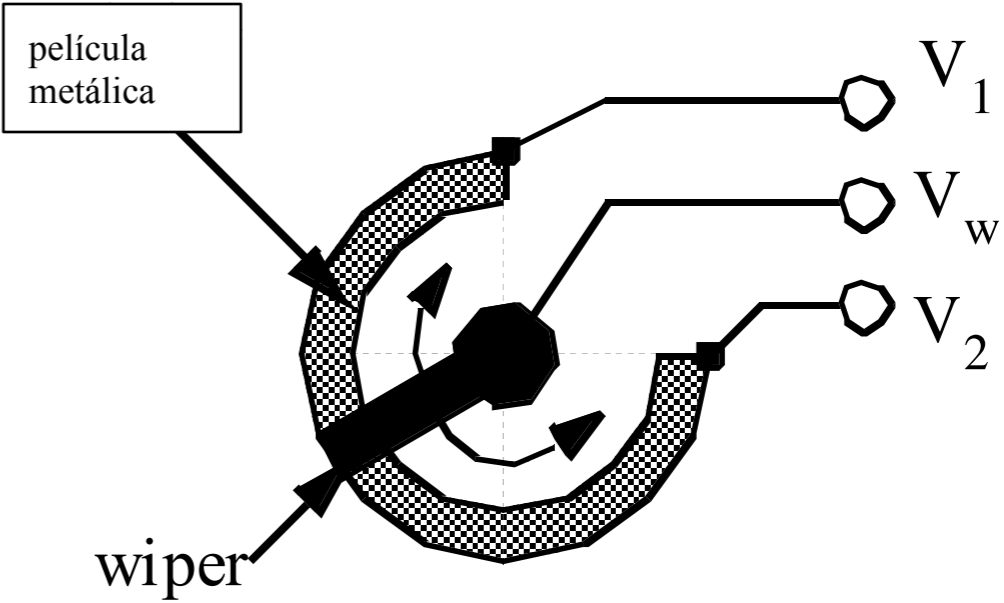


Application: Foil packaging machines
Product: Sensor potentiometer SP2841

Potenciometros

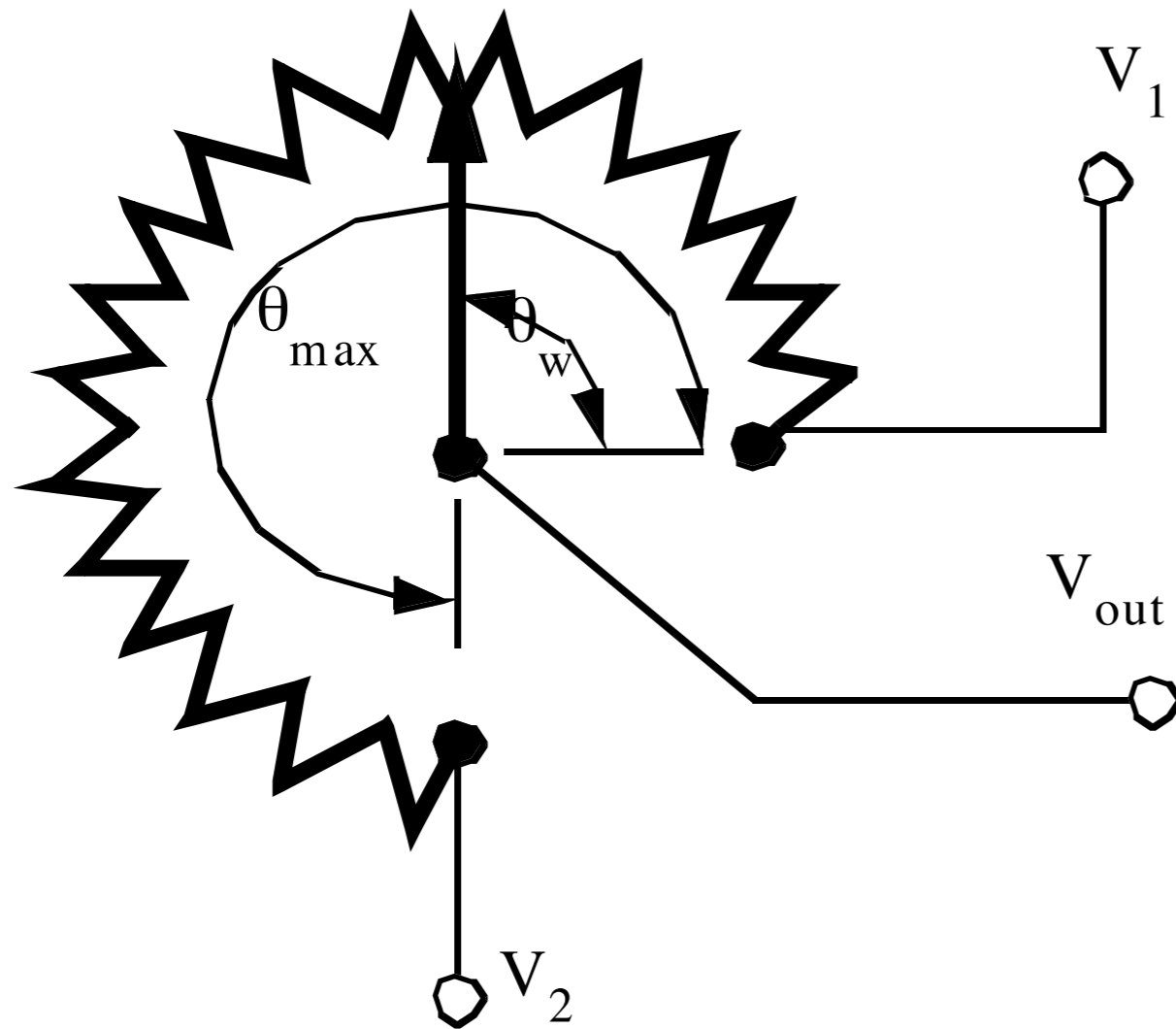


esquemático



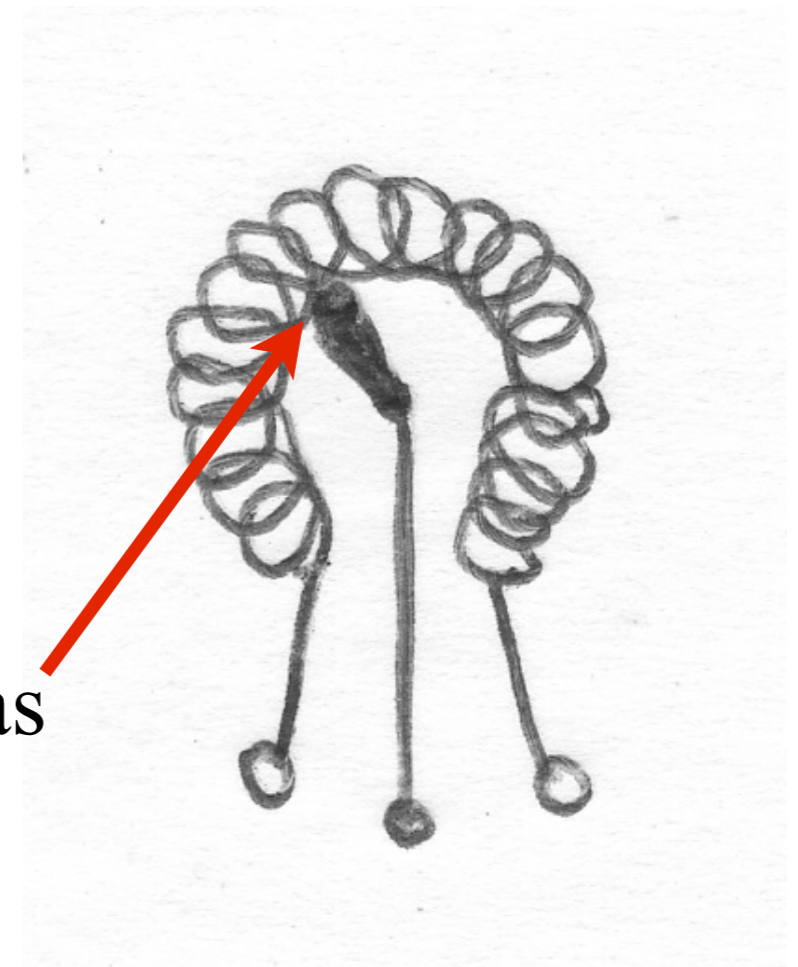
- Requieren un “dead-zone” al final de la vuelta
- Pots de instrumentación: hasta de 358° de rotación útil (versus $\sim 270^\circ$ de uno normal)
- Hay muchos tipos: “slide-wire”, película de metal, película de carbon, “wire-wound”, película plástica, cermet (mezcla de cerámica-metal)
- “Wire-wound” son comunes para medir ángulo o translación

Relación de Voltajes



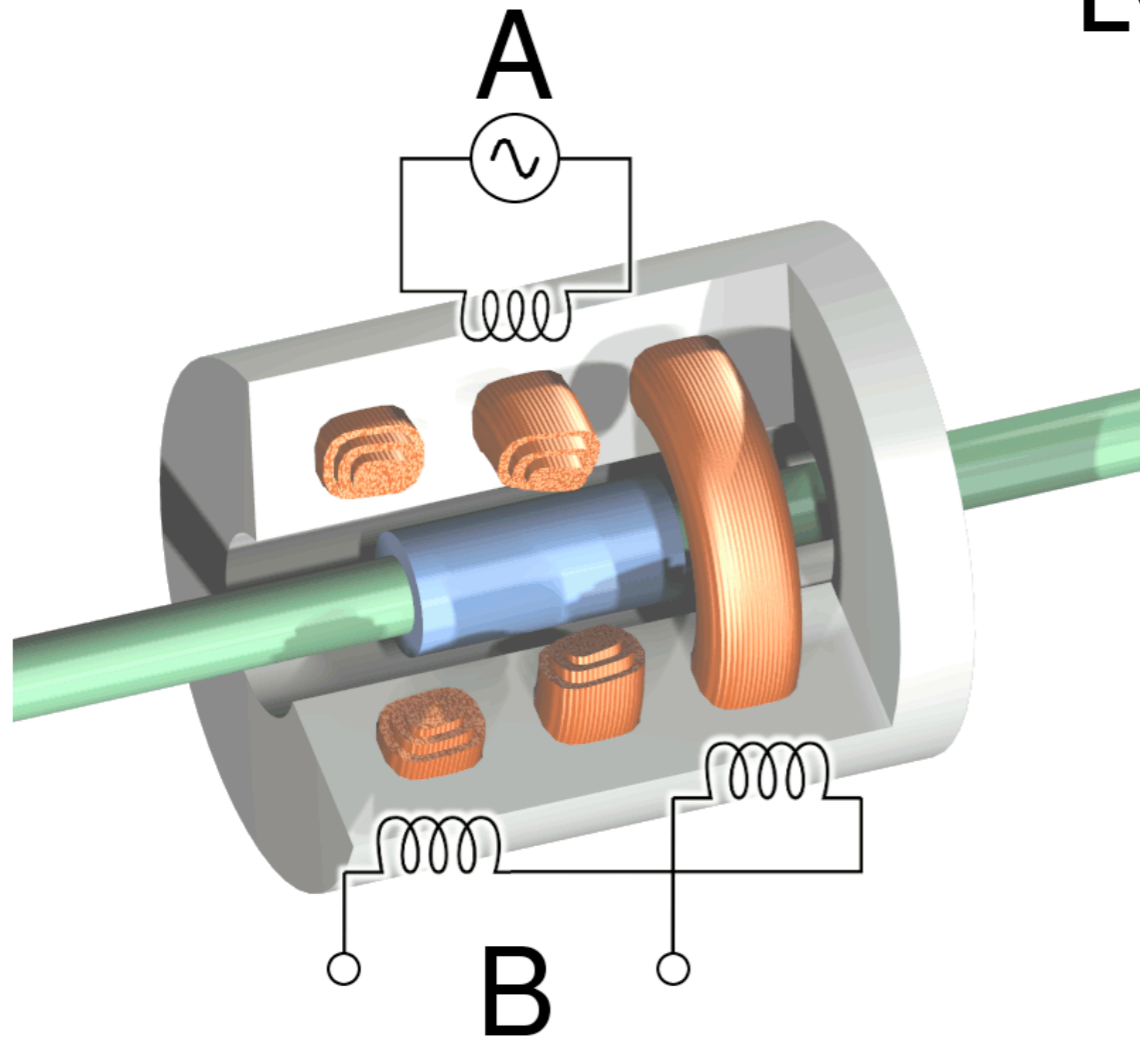
$$V_{out} = (V_2 - V_1) \left(\frac{\theta_w}{\theta_{max}} \right) + V_1$$

Half-step - contacto entre dos vueltas



- Ejemplo: Considere un potenciómetro tipo w.w. con 100 vueltas, cada una con una resistencia de $\frac{1}{2}\Omega$. Determine la fracción del voltaje total que aparece en el *wiper* cuando esta en (a) la 20^{va} vuelta; (b) entre la vuelta 20 y 21; (c) en la 70^{va} vuelta; (d) entre la vuelta 70 y 71.
- Ejemplo: potenciómetro w.w. con 700 vueltas y rango de 350° . La resolución es $350^\circ/700$ vueltas = $\frac{1}{2}^\circ/\text{vuelta}$. Si un voltaje DC de 10V se conecta al potenciómetro y la resistencia total es $15\text{k}\Omega$, la sensibilidad promedio es $10\text{V}/350^\circ = 29\text{mV}/^\circ$. El consumo de potencia es $(10\text{V})^2/15\text{k}\Omega = 670 \text{ mW}$ más la potencia consumida por la carga.

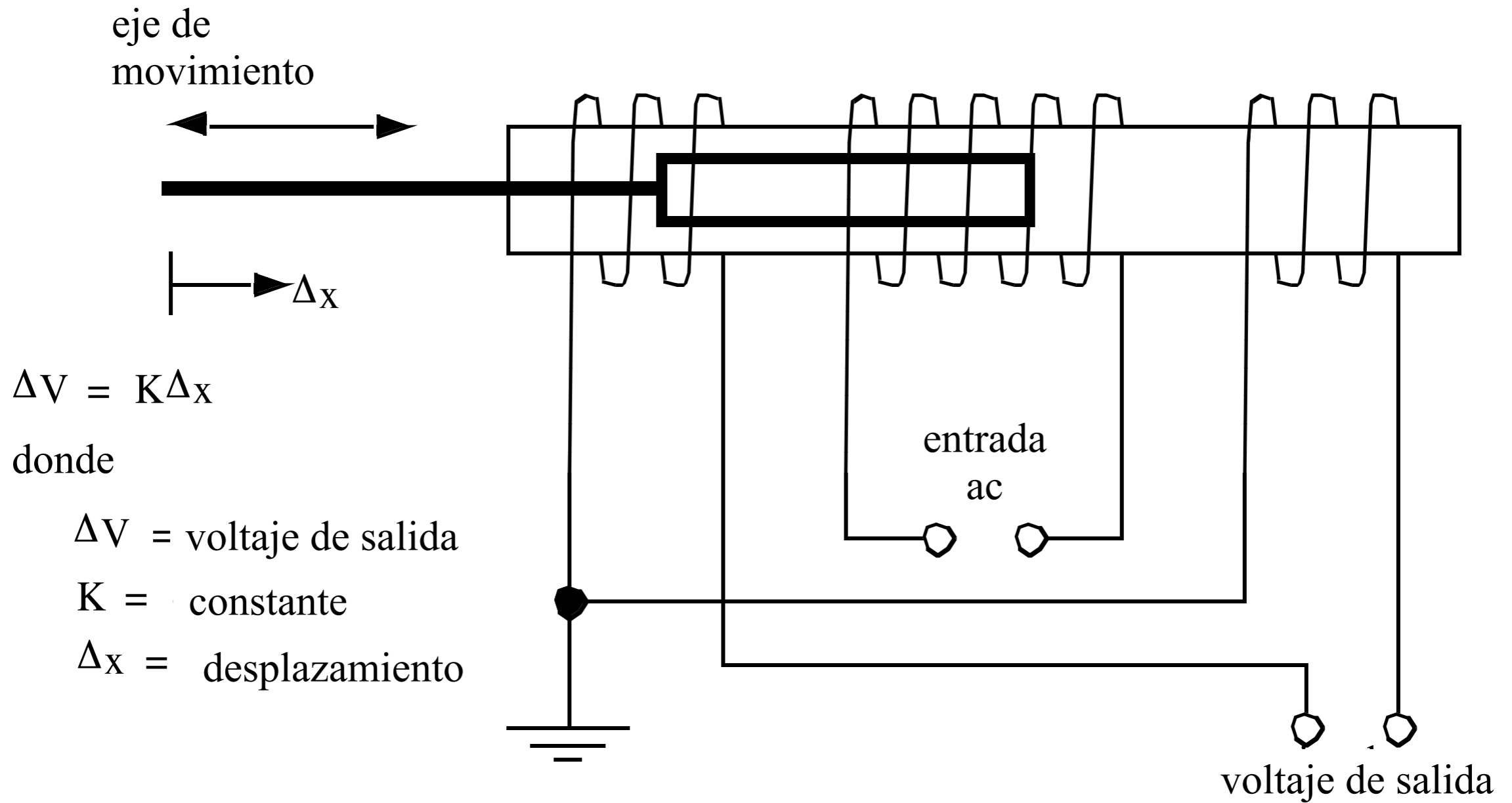
LVDT



Corriente alterna fluye en la bobina principal *A* e induce corriente en las bobina secundaria *B* que depende de la posición del *core* magnético.

Las bobinas secundarias están conectadas en serie-reverso y producen un voltaje diferencial.

Linear Variable Differential Transformer (LVDT)



Ventajas de LVDT

- No tiene fricción
- Larga vida mecánica
- Resolución ilimitada (determinada por la electrónica)
- Excitación y salida están eléctricamente aisladas

Desventaja

- Costoso y requiere electrónica compleja

Reference: $r(t) = \cos(\omega_0 t)$

Measurement: $m(t)$

$$\begin{aligned} F(s) &= \int_0^{\infty} f(t) \exp(-st) dt \\ &= \int_0^{\infty} m(t) \cos(\omega_0 t) \exp(-st) dt \\ &= \frac{1}{2} \left(\int_0^{\infty} m(t) \exp(j\omega_0 t - st) dt + \int_0^{\infty} m(t) \exp(-j\omega_0 t - st) dt \right) \\ &= \frac{1}{2} (M(\omega - \omega_0) + M(\omega + \omega_0)) \end{aligned}$$

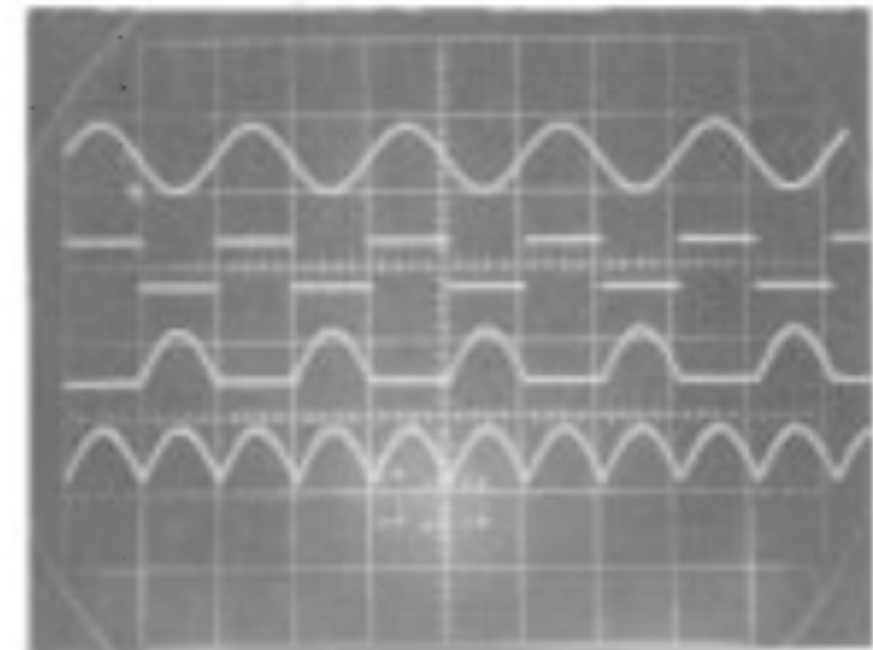
Figure 6b shows a circuit which does this. Waveforms of operation are given in Figure 6c. In this circuit, Q1 and its associated components from a phase shift oscillator which runs at 2.5 kHz, the manufacturer's specified transducer operating frequency. A1A amplifies and buffers Q1's output and drives the LVDT (waveform A, Figure 6c). Since the transducer's output will vary with drive level, feedback is used to stabilize the 2.5 kHz amplitude. A1C and A1D full wave rectify a sample of the drive waveform. A1C's filtered output is applied to A1D, a servo amplifier. A1D compares A1C's output to the LM329 reference and drives the Q1 oscillator to complete an amplitude stabilization loop. The LVDT's output is amplified by A2C and fed to A2A. A2A is a unity gain amplifier whose sign alternates between "+" and "-". Synchronous switching for A2A comes from C1 (waveform B, Figure 6c), which is driven by the modulation sine wave output via a phase shift network. The phase trim network compensates phase shift in the LVDT and ensures that C1 switches at the zero crossings relative to A2A's output. When C1's output is low, the 2N4393 FET is off and A2A's positive input (waveform C, Figure 6c) receives signal. When the sine wave reverses polarity, C1's output goes high, turning on the FET, which grounds A2A's "+" input. Under these conditions A2A is always switching its amplification's sign from "+" to "-" in synchronism with the sine wave output from the LVDT. A2A's phase sensitive output, in this case positive, appears in trace D, Figure 6c. A2B provides a scaled and filtered DC output. To trim the circuit, set the LVDT to at least 1/2 physical displacement and adjust the phase trim for maximum output indication. Next, adjust the gain trim for the desired circuit output at full-scale LVDT displacement.

A = 5V/DIV

B = 50V/DIV

C = 5V/DIV

D = 5V/DIV



HORIZONTAL = 200 μs / DIV

TL/H/5641-12

FIGURE 6c

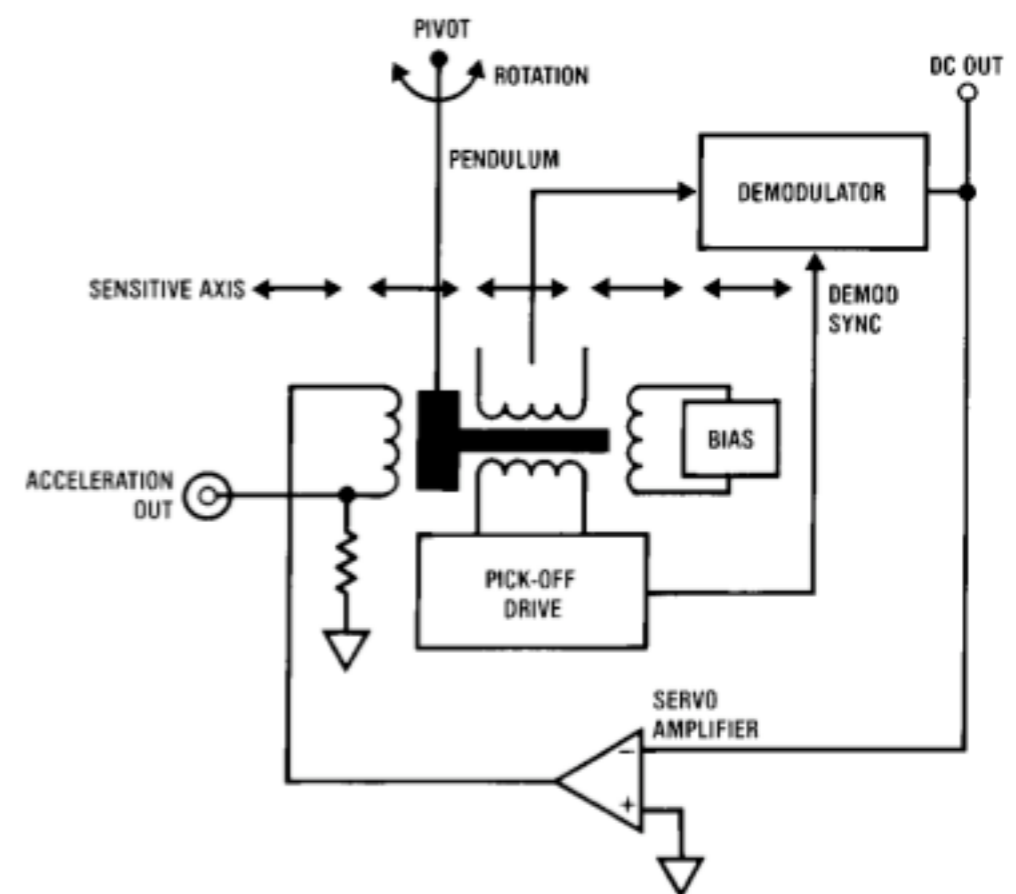
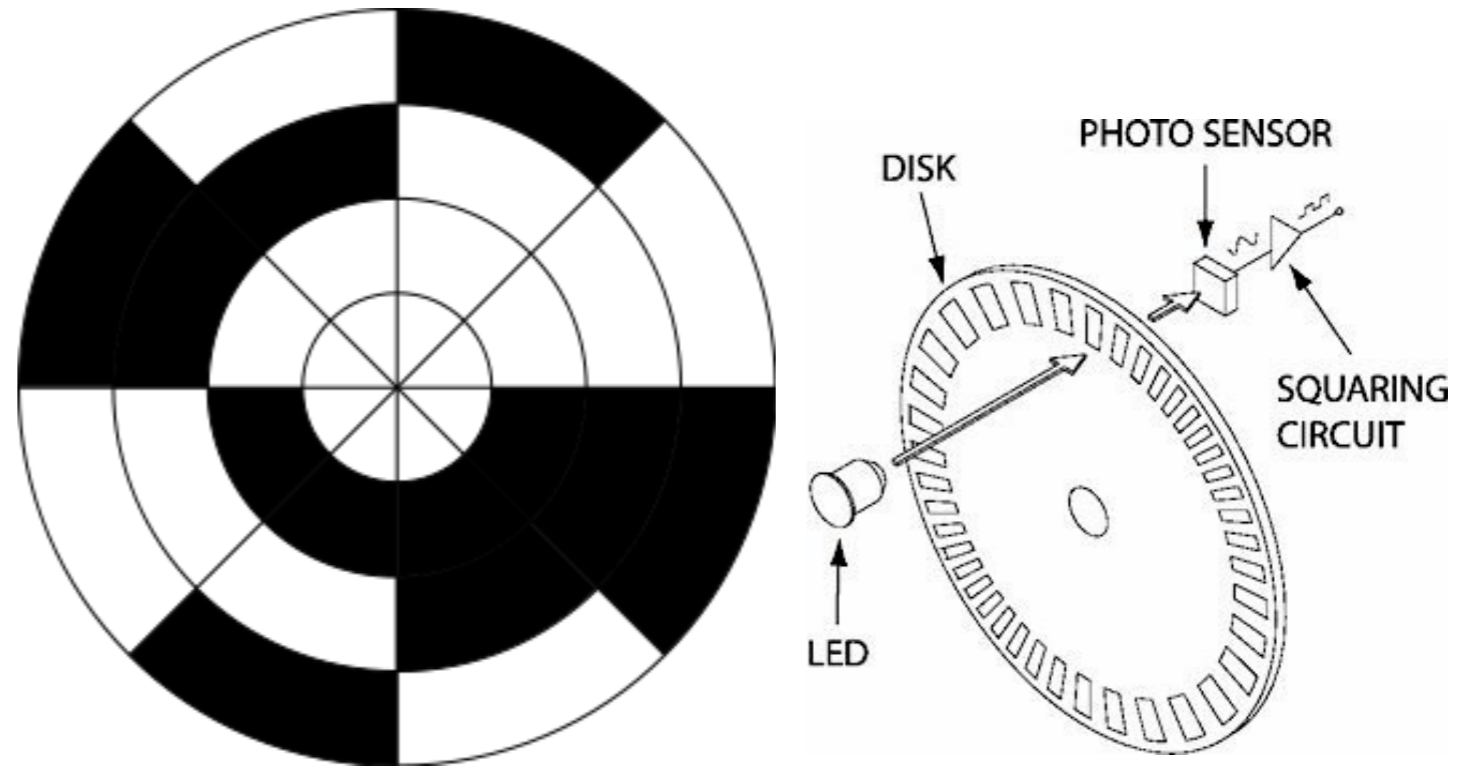


FIGURE 7a

TL/H/5641-11

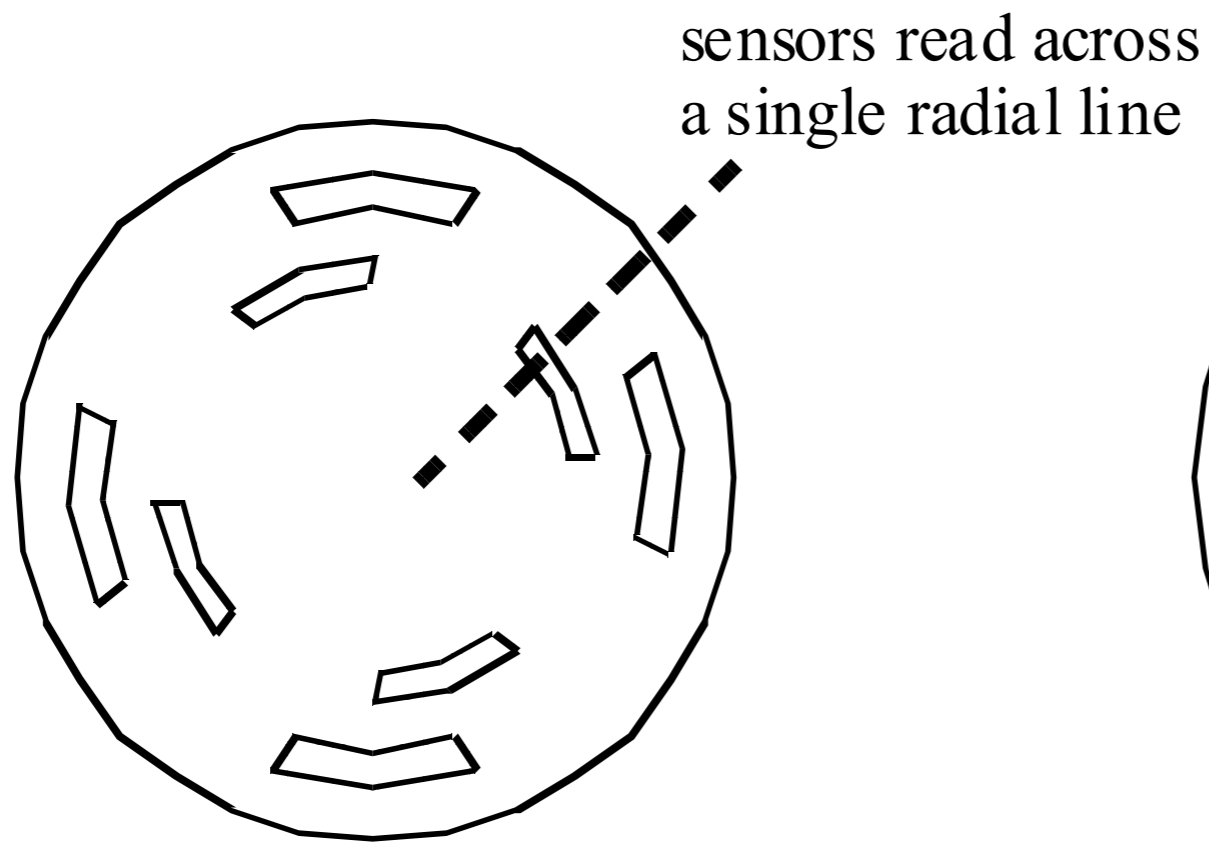
Codificador óptico



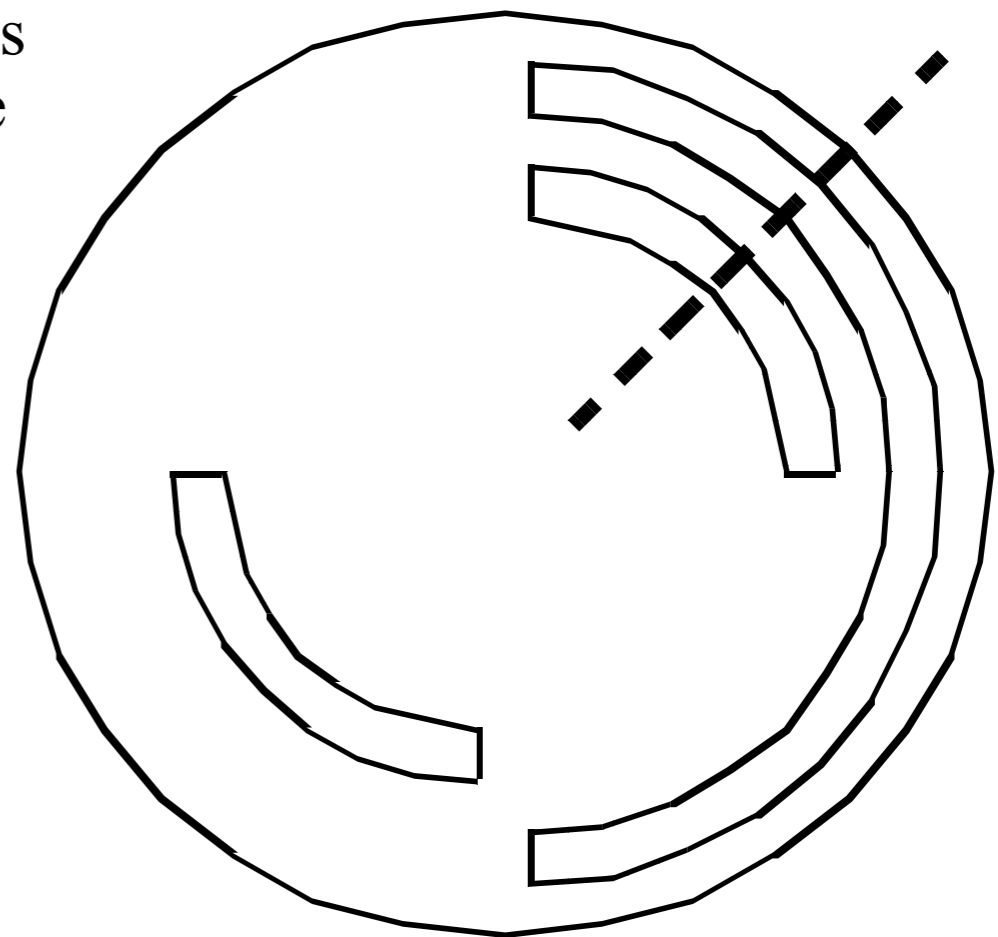
Ejemplo

- ⇒ codificador de 3-bits
- ⇒ provee 8 angulos distintos
- ⇒ resolución = $360^\circ / 2^N =$
 $360^\circ / 2^3 = 360^\circ / 8 = 45^\circ$

Absolute and Relative Encoders

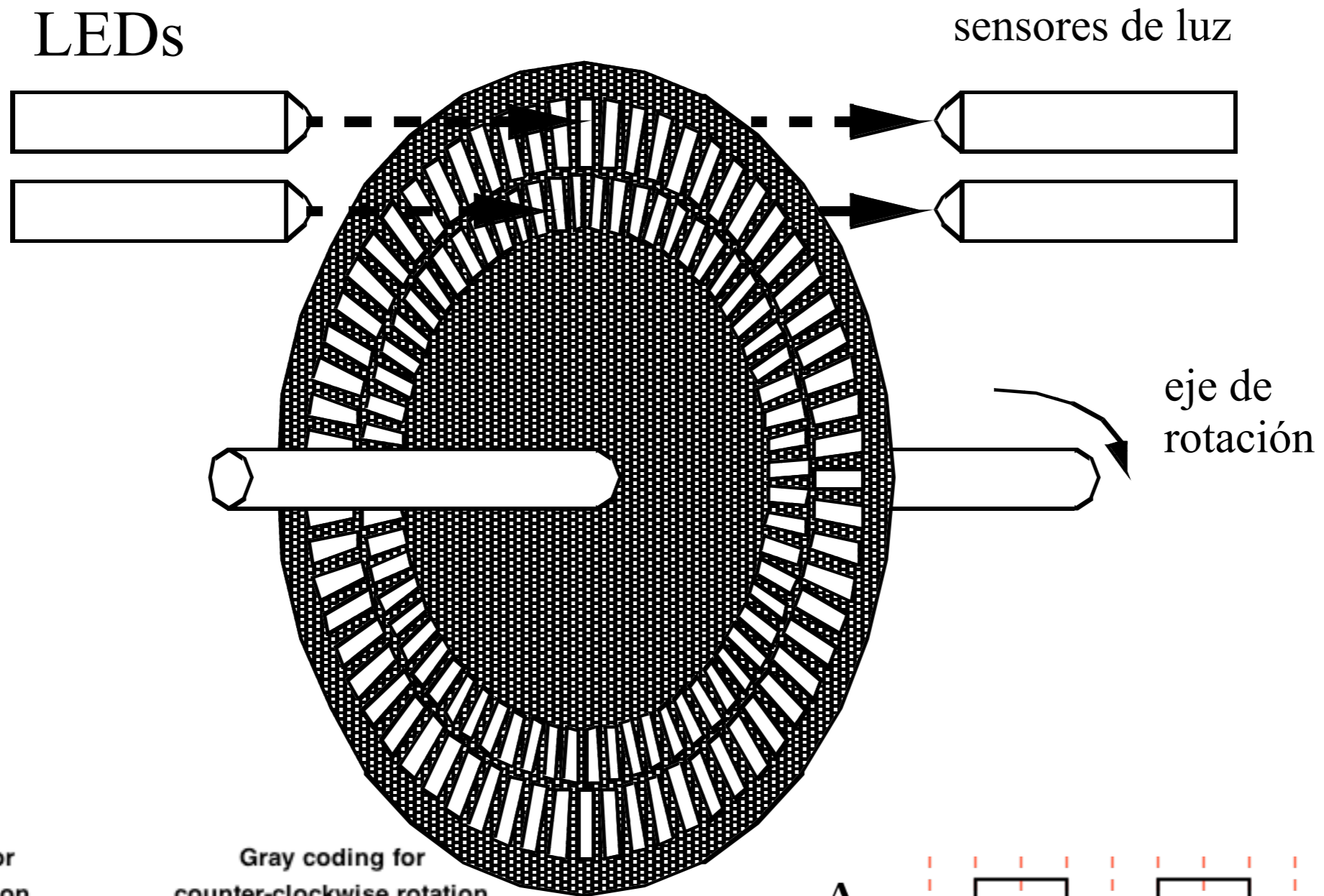


relative encoder
(quadrature)



absolute encoder

Codificadores ópticos

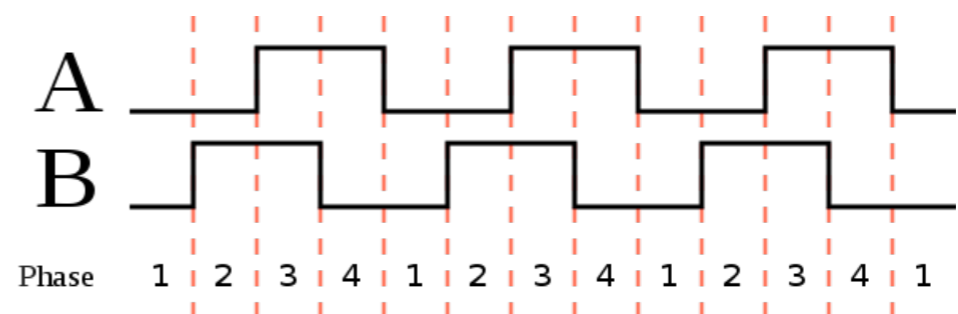


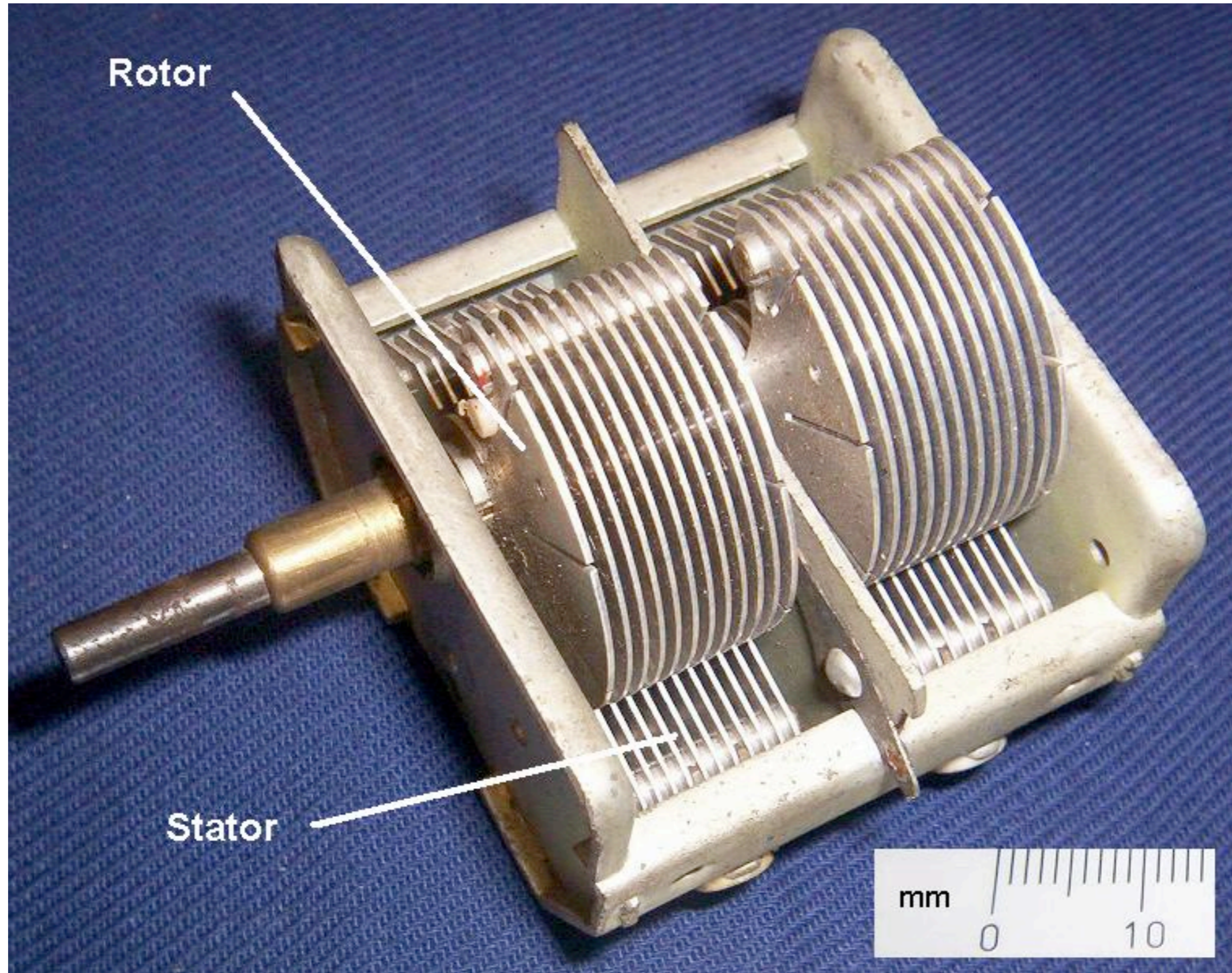
Gray coding for clockwise rotation

Phase	A	B
1	0	0
2	0	1
3	1	1
4	1	0

Gray coding for counter-clockwise rotation

Phase	A	B
1	1	0
2	1	1
3	0	1
4	0	0





Variable capacitor