

# **INEL 4206: Microprocessors**

## **Primavera de 2003**

### **Temas a estudiar para el examen parcial 2**

**EL examen 2 incluye todo el material cubierto por el examen 1.**

#### **Programming the Easy I**

- + Easy I assembly language programming
- + Easy I machine code programming
- + Lecturas:
  - + Transparencias de Lecture 4
  - + Notas de la clase
  - + Problem set 2

#### **Practical Universal Computers**

- + von Neumann Architecture
- + CPU Performance Metrics
  - + Cycle Time
  - + CPI
  - + Execution time
- + Data Path Design
  - + Basic Building Blocks:
    - + Registers, muxes, buses, ALU's, etc.
    - + Edge triggered components and their impact on CPU design
- + Control Unit Design and Implementation
  - + Level 0, 1, 2 flowcharts
  - + Timing and sequencing of operations. Timing diagrams.
  - + Register Transfer Language
  - + State Transition Tables
  - + Memory interfacing
- + Lecturas:
  - + Transparencias de Lectura 4 (website)
  - + Notas de la clase
  - + Capítulo 2 de Patterson & Hennessy
  - + Capítulo 5 de Patterson & Hennessy
  - + Apéndice B de Patterson & Hennessy